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Day: Thursday Date: 7/27/2006 Time: 07:52:08

Inventor Name Search Result

Your Search was:

Last Name = BARR

First Name = ANDREW

			V		
Application#	Patent#	Status			Inventor Name
10370537	6970001	150	02/20/2003	VARIABLE IMPEDANCE TEST PROBE	BARR, ANDREW
<u>10684646</u>	Not Issued	71	10/14/2003	Power distribution system	BARR, ANDREW
06651047	4628302	150	09/14/1984	LIQUID LEVEL DETECTION SYSTEM	BARR, ANDREW B.
11164200	Not Issued	30	11/14/2005	SEAT IDENTIFICATION SYSTEM	BARR, ANDREW D.
07129437	5173699	150	11/05/1987	ANTENNA ARRANGEMENT	BARR, ANDREW D.
07167485	Not Issued	89	02/22/1988	RADAR SYSTEM	BARR, ANDREW D.
07833296	Not Issued	161	02/11/1992	FLUID MONITORING APPARATUS	BARR, ANDREW D.
<u>09923735</u>	6559733	150		REDUCING EFFECTS OF ELECTRICAL IMPEDANCE	BARR, ANDREW H.
10216229	Not Issued	71		System and method for managing the operating frequency of blades in a bladed-system	BARR, ANDREW H.
10216232	7055044	150	08/12/2002	SYSTEM AND METHOD FOR VOLTAGE MANAGEMENT OF A PROCESSOR TO OPTIMIZE PERFORMANCE AND POWER DISSIPATION	BARR, ANDREW H.
10216233	7080263	150		VOLTAGE MANAGEMENT OF PROCESSORS IN A BLADED SYSTEM BASED ON NUMBER OF LOADED PROCESSORS	BARR, ANDREW H.
10216234	Not Issued	71		System and method for load dependent frequency and performance modulation in bladed systems	BARR, ANDREW H.
10216283	7076671	150	08/12/2002	MANAGING AN OPERATING FREQUENCY OF PROCESSORS IN A MULTI-PROCESSOR COMPUTER SYSTEM	BARR, ANDREW H.
10216284	6983386	150	08/12/2002	VOLTAGE MANAGEMENT OF	BARR, ANDREW H.

				BLADES IN A BLADED ARCHITECTURE SYSTEM BASED ON THERMAL AND POWER BUDGET ALLOCATION	
10216285	Not Issued	100		BLADE SYSTEM FOR USING MULTIPLE FREQUENCY SYNTHESIZERS TO CONTROL MULTIPLE PROCESSOR CLOCKS OPERATING AT DIFFERENT FREQUENCIES BASED UPON USER INPUT	BARR, ANDREW H.
10216286	Not Issued	95	II ' I	SYSTEM AND METHOD FOR MANAGING PROCESSOR VOLTAGE IN A MULTI-PROCESSOR COMPUTER SYSTEM FOR OPTIMIZED PERFORMANCE	BARR, ANDREW H.
10216435	6948043	150	II	MANAGEMENT OF A MEMORY SUBSYSTEM	BARR, ANDREW H.
10216437	7058828	150		SYSTEM, METHOD AND APPARATUS FOR THE FREQUENCY MANAGEMENT OF BLADES IN A BLADED ARCHITECTURE BASED ON PERFORMANCE REQUIREMENTS	BARR, ANDREW H.
10216438	Not Issued	30		System and method for the frequency management of computer systems to allow capacity on demand	BARR, ANDREW H.
10328805	Not Issued	41	12/23/2002	Enabling multiple testing devices	BARR, ANDREW H.
10328906	Not Issued	71	12/23/2002	Automatic detection of different microprocessor architectures	BARR, ANDREW H.
10358903	Not Issued	120		Method and apparatus for improving signal integrity in a high speed flex cable	BARR, ANDREW H.
10453594	Not Issued	161	06/04/2003	Apparatus and method for detecting and rejecting high impedance interconnect failures in manufacturing process	BARR, ANDREW H.
10453595	6940288	150	06/04/2003	APPARATUS AND METHOD FOR MONITORING AND PREDICTING FAILURES IN SYSTEM INTERCONNECT	BARR, ANDREW H.
10453610	6895353	150	06/04/2003	APPARATUS AND METHOD FOR MONITORING HIGH IMPEDANCE FAILURES IN CHIP INTERCONNECTS	BARR, ANDREW H.
10453612	6879173	150	06/04/2003	APPARATUS AND METHOD FOR DETECTING AND REJECTING HIGH IMPEDANCE FAILURES IN CHIP INTERCONNECTS	BARR, ANDREW H.
10453613	Not Issued	164	06/04/2003	APPARATUS AND METHOD FOR DETECTING HIGH IMPEDANCE	BARR, ANDREW H.

				FAILURES IN SYSTEM INTERCONNECT	
10459231	6791843	150	06/11/2003	PARALLEL BOARD CONNECTION SYSTEM AND METHOD	BARR, ANDREW H.
10622917	6957544	150	07/18/2003	METHOD AND APPARATUS FOR REGULATING THE OPERATING TEMPERATURE OF ELECTRONIC DEVICES	BARR, ANDREW H.
10631696	6961242	150	07/31/2003	SYSTEM FAN MANAGEMENT BASED ON SYSTEM LOADING OPTIONS FOR A SYSTEM HAVING REPLACEABLE ELECTRONICS MODULES	BARR, ANDREW H.
10632218	Not Issued	20	II (Heat sink fan management based on performance requirements	BARR, ANDREW H.
10646078	Not Issued	71		Bus clock frequency management based on characteristics of an application program	BARR, ANDREW H.
10646079	Not Issued	30		Bus clock frequency management based on device load	BARR, ANDREW H.
10646099	Not Issued	93	08/22/2003	BUS CLOCK FREQUENCY MANAGEMENT BASED ON DEVICE BANDWIDTH CHARACTERISTICS	BARR, ANDREW H.
10652536	Not Issued	61	08/29/2003	System and method for testing a memory	BARR, ANDREW H.
10698204	6985826	150	10/31/2003	SYSTEM AND METHOD FOR TESTING A COMPONENT IN A COMPUTER SYSTEM USING VOLTAGE MARGINING	BARR, ANDREW H.
10699423	Not Issued	61	10/31/2003	System and method for testing a cell	BARR, ANDREW H.
10703306	Not Issued	41		System and method for testing a component in a computer system using frequency margining	BARR, ANDREW H.
10714302	Not Issued	30		System and method for testing a memory using DMA	BARR, ANDREW H.
10714386	Not Issued	61	11/14/2003	System and method for testing a memory with an expansion card using DMA	BARR, ANDREW H.
10727440	7072788	150	12/04/2003	SYSTEM AND METHOD FOR TESTING AN INTERCONNECT IN A COMPUTER SYSTEM	BARR, ANDREW H.
11033751	6995581	150	01/13/2005	APPARATUS AND METHOD FOR DETECTING AND REJECTING HIGH IMPEDANCE FAILURES IN CHIP INTERCONNECTS	BARR, ANDREW H.
11109309	Not Issued	30	04/19/2005	Redundant I/O interface management	BARR, ANDREW H.
11109352	Not Issued	20	04/19/2005	Computing with both lock-step and free- step processor modes	BARR, ANDREW H.

11109353	Not Issued	30		External state cache for computer processor	BARR, ANDREW H.
11191052	Not Issued	20		System fan management based on system loading options for a system having replaceable electronics modules	BARR, ANDREW H.
10263985	6970054	150	10/02/2002	· ·	BARR, ANDREW HARVEY
10298876	Not Issued	161	11/18/2002	Cable systems and related methods	BARR, ANDREW HARVEY
10308533	6676417	150		SYSTEMS AND METHODS FOR INTERCONNECTING ELECTRONIC COMPONENTS	BARR, ANDREW HARVEY
10351950	6680853	150		SYSTEMS AND METHODS FOR MOUNTING COMPONENTS TO CIRCUIT ASSEMBLIES	BARR, ANDREW HARVEY

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Inventor Name Search Result

Your Search was:

Last Name = ESPINOZA-IBARRA

First Name = RICARDO

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>10216229</u>	Not Issued	71	08/12/2002	System and method for managing the operating frequency of blades in a bladed-system	ESPINOZA-IBARRA, RICARDO
10216232	7055044	150	08/12/2002	SYSTEM AND METHOD FOR VOLTAGE MANAGEMENT OF A PROCESSOR TO OPTIMIZE PERFORMANCE AND POWER DISSIPATION	ESPINOZA-IBARRA, RICARDO
10216233	7080263	150	08/12/2002	VOLTAGE MANAGEMENT OF PROCESSORS IN A BLADED SYSTEM BASED ON NUMBER OF LOADED PROCESSORS	ESPINOZA-IBARRA, RICARDO
10216234	Not Issued	71		System and method for load dependent frequency and performance modulation in bladed systems	ESPINOZA-IBARRA, RICARDO
10216283	7076671	150	08/12/2002	MANAGING AN OPERATING FREQUENCY OF PROCESSORS IN A MULTI-PROCESSOR COMPUTER SYSTEM	ESPINOZA-IBARRA, RICARDO
10216284	6983386	150	08/12/2002	VOLTAGE MANAGEMENT OF BLADES IN A BLADED ARCHITECTURE SYSTEM BASED ON THERMAL AND POWER BUDGET ALLOCATION	ESPINOZA-IBARRA, RICARDO
<u>10216285</u>	Not Issued	100		BLADE SYSTEM FOR USING MULTIPLE FREQUENCY SYNTHESIZERS TO CONTROL MULTIPLE PROCESSOR CLOCKS OPERATING AT DIFFERENT FREQUENCIES BASED UPON USER INPUT	ESPINOZA-IBARRA, RICARDO
10216286	Not Issued	95	08/12/2002	SYSTEM AND METHOD FOR MANAGING PROCESSOR VOLTAGE IN A MULTI-PROCESSOR COMPUTER SYSTEM FOR OPTIMIZED PERFORMANCE	ESPINOZA-IBARRA, RICARDO
10216435	6948043	150	08/12/2002	MANAGEMENT OF A MEMORY SUBSYSTEM	ESPINOZA-IBARRA, RICARDO

10216437	7058828	150		SYSTEM, METHOD AND APPARATUS FOR THE FREQUENCY MANAGEMENT OF BLADES IN A BLADED ARCHITECTURE BASED ON PERFORMANCE REQUIREMENTS	ESPINOZA-IBARRA, RICARDO
10216438	Not Issued	30			ESPINOZA-IBARRA, RICARDO
10263587	Not Issued	61		T	ESPINOZA-IBARRA, RICARDO
10263985	6970054	150	10/02/2002	APPARATUS FOR TERMINATING TRANSMISSION LINES TO REDUCE ELECTROMAGNETIC INTERFERENCE IN AN ELECTRONIC SYSTEM	ESPINOZA-IBARRA, RICARDO
10360329	Not Issued	61		Radio frequency linked computer architecture	ESPINOZA-IBARRA, RICARDO
10631696	6961242	150			ESPINOZA-IBARRA, RICARDO
10632218	Not Issued	20		Heat sink fan management based on performance requirements	ESPINOZA-IBARRA, RICARDO
10646078	Not Issued	71		Bus clock frequency management based on characteristics of an application program	ESPINOZA-IBARRA, RICARDO
<u>10646079</u>	Not Issued	30		Bus clock frequency management based on device load	ESPINOZA-IBARRA, RICARDO
10646099	Not Issued	93		BUS CLOCK FREQUENCY MANAGEMENT BASED ON DEVICE BANDWIDTH CHARACTERISTICS	ESPINOZA-IBARRA, RICARDO
10653377	7054156	150	09/02/2003	FAN ROTOR SYSTEMS HAVING COLLAPSIBLE FAN BLADES	ESPINOZA-IBARRA, RICARDO
10678464	Not Issued	30		1 1 5	ESPINOZA-IBARRA, RICARDO
10678657	Not Issued	71	10/03/2003		ESPINOZA-IBARRA, RICARDO
10681721	Not Issued	71			ESPINOZA-IBARRA, RICARDO
<u>10741906</u>	Not Issued	30		Rack equipment power purchase plan supervision system and method	ESPINOZA-IBARRA, RICARDO
10742495	Not Issued	30	12/18/2003	1 1	ESPINOZA-IBARRA, RICARDO
10829072	Not Issued	40		Rack equipment capacity on demand system and method	ESPINOZA-IBARRA, RICARDO
11033083	Not Issued	41	1		ESPINOZA-IBARRA, RICARDO

11260095	Not Issued	20		Electronics cooling fan with collapsible fan blade	ESPINOZA-IBARRA, RICARDO
11260105	Not Issued	20	10/26/2005	Centrifugal fan clutch for an electronics cooling fan	ESPINOZA-IBARRA, RICARDO
10452222	Not Issued	61	06/03/2003	Apparatus and method for mounting a surface mount component in an etched well in a printed circuit board	ESPINOZA-IBARRA, RICARDO E.
10602972	6987370	150	06/23/2003	METHOD AND SYSTEM FOR COOLING ELECTRONIC COMPONENTS	ESPINOZA-IBARRA, RICARDO E.
10612523	Not Issued	30	07/02/2003	Apparatus and method for real-time power distribution management	ESPINOZA-IBARRA, RICARDO E.
10684768	Not Issued	41	10/14/2003	Server card power switch	ESPINOZA-IBARRA, RICARDO E.
10685175	Not Issued	41	10/14/2003	Power distribution system	ESPINOZA-IBARRA, RICARDO E.
10769049	Not Issued	161	01/29/2004	Information filtering device	ESPINOZA-IBARRA, RICARDO E.
10801247	Not Issued	30		Rack equipment power pricing plan control system and method	ESPINOZA-IBARRA, RICARDO E.
10623015	Not Issued	161		Method and system for independently displaying a plurality of visual signals	ESPINOZA-IBARRA, RICARDO ERNESTO
10698904	6956344	150	10/31/2003	HIGH AVAILABILITY FAN SYSTEM	ESPINOZA-IBARRA, RICARDO ERNESTO
10699430	Not Issued	30		Scalable, modular, high availability fan system	ESPINOZA-IBARRA, RICARDO ERNESTO

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Inventor Name Search Result

Your Search was:

Last Name = SOMERVILL

First Name = KEVIN

		G	D . D . 1		T N
Application#	=======================================	=			Inventor Name
<u>10619912</u>	Not Issued	164	07/15/2003	METHOD AND APPARATUS FOR TESTING AN ELECTRONIC DEVICE	SOMERVILL, KEVIN
10459075	Not Issued	61	06/11/2003	Computer system	SOMERVILL, KEVIN M.
10459172	Not Issued	93	06/11/2003	MULTI-COMPUTER SYSTEM	SOMERVILL, KEVIN M.
10646079	Not Issued	30	08/22/2003	Bus clock frequency management based on device load	SOMERVILL, KEVIN M.
10646099	Not Issued	93	08/22/2003	BUS CLOCK FREQUENCY MANAGEMENT BASED ON DEVICE BANDWIDTH CHARACTERISTICS	SOMERVILL, KEVIN M.
10808881	Not Issued	93	03/24/2004	DEVICE BANDWIDTH MANAGEMENT USING A BUS CONFIGURATION MULTPLEXER	SOMERVILL, KEVIN M.
10777505	Not Issued	41	02/11/2004	Power distribution system utilizing redundant AC sources or DC sources	SOMERVILL, KEVIN MICHAEL
<u>10777917</u>	Not Issued	30		Power distribution system having redundant mixed sources and method	SOMERVILL, KEVIN MICHAEL
60300843	Not Issued	159		Passively transferred antibody in serum of hatchlings	SOMERVILLE, KEVIN
10185590	6729265	150	06/27/2002	SUPPLEMENTED ANTIBODY FEED TO ENTER THE CIRCULATING SYSTEM OF NEWBORNS	SOMERVILLE, KEVIN C.
10384812	Not Issued	161	03/10/2003	Antibody-containing blend obtained from eggs	SOMERVILLE, KEVIN C.
10628087	Not Issued	160	l l	Product for treatment of viral infection in fish	SOMERVILLE, KEVIN C.
60398619	Not Issued	159	i i	Product for treatment of viral infection in fish	SOMERVILLE, KEVIN C.
60531916	Not Issued	159		Product for treatment of viral infection in fish	SOMERVILLE, KEVIN C.

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